

**LOCATION-BASED ALLOCATION OF MEMORY RESOURCES
IN MEMORY MAPPED INPUT/OUTPUT FABRIC**

Abstract of the Disclosure

5 An apparatus, program product and method in which a memory address space is allocated non-uniformly to IO resources in a memory mapped IO fabric based upon the locations of individual IO endpoints to which such IO resources are coupled. In a PCI-based environment, for example, PCI adapters are allocated memory address ranges in a PCI bus address space based upon the locations of the particular slots within which the PCI adapters are mounted.